

In re application: Wen-Yen Hwang
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Reply to Office action of 10/28/2004

LISTING OF CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (original) A method for fabricating a surface-emitting laser having current confinement, the method comprising the steps of:

- (a) providing a first laser portion having a substrate, a semiconductor active region, and a bottom mirror disposed between the substrate and the active region;
- (b) epitaxially growing a first top spacer layer on the active region, the first top spacer layer comprising a current-spreading buffer layer disposed on the active region, a current-confinement layer disposed on the buffer layer, and a current-spreading platform layer disposed on the current-confinement layer, wherein the combined thickness of the platform and current-confinement layers is less than the thickness of the buffer layer;
- (c) forming, in the current-confinement layer, a current-confinement structure having an annular region of enhanced resistivity and a central aperture of comparatively lower resistivity, said step of forming comprising the step of performing ion implantation; and
- (d) after said forming of the current-confinement structure, performing epitaxial regrowth to form a top current-spreading layer.

2. (original) The method of claim 1, wherein the surface-emitting laser is a vertical-cavity surface-emitting laser designed to emit coherent light having a wavelength of about 1550nm.

3. (original) The method of claim 1, wherein said platform layer and said substrate consist of InP and said current-confinement layer consists of one of InAlAs and InGaAlAs.

4. (original) The method of claim 1, further comprising the step of forming a top mirror disposed axially above the first top spacer layer to complete a resonant cavity surrounding the active region.

5. (original) The method of claim 4, wherein the top mirror is a DBR having a plurality of mirror pairs.

6. (original) The method of claim 5, wherein the DBR is a dielectric DBR having a plurality of dielectric mirror pairs, wherein said step of forming said dielectric DBR comprises the step of depositing said dielectric mirror pairs.

7. (original) The method of claim 5, wherein the DBR is a semiconductor DBR having a plurality of semiconductor mirror pairs, wherein said step of forming said semiconductor DBR comprises the step of epitaxially regrowing said semiconductor mirror pairs.

8. (original) The method of claim 1, further comprising the step of:

- (e) forming a metal contact on the laser so that the metal contact is electrically coupled with the top current-spreading layer, wherein:

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the top current-spreading layer and the platform layer together are sufficiently conductive to conduct pumping current from the metal contact laterally through the combined top current-spreading layer and platform layer to the central aperture of the current-confinement structure; and
the annular region has an enhanced resistivity sufficiently greater than that of the central aperture so that at least about 70% of the pumping current from the metal contact conducted through the top current-spreading layer and platform layer is blocked by the annular region and forced through the central aperture.

9. (original) The method of claim 8, further comprising the step of forming a top mirror disposed axially above the first top spacer layer to complete a resonant cavity surrounding the active region.

10. (original) The method of claim 9, wherein the top mirror is a DBR having a plurality of mirror pairs.

11. (original) The method of claim 9, wherein the surface-emitting laser is a vertical-cavity surface-emitting laser designed to emit coherent light having a wavelength of about 1550nm.

12. (original) The method of claim 8, further comprising the step of:

(f) forming a contact-facilitating layer on the top current-spreading layer by performing epitaxial regrowth;

wherein step (e) comprises the step of forming the metal contact on the contact-facilitating layer, whereby the metal contact is electrically coupled with the top current-spreading layer via the contact-facilitating layer.

13. (original) The method of claim 8, wherein the resistance of the annular region to the pumping current conducted through said top current-spreading layer and said platform layer from the metal contact is sufficiently greater than the resistance of the central aperture to said pumping current so that most of said pumping current flows through the central aperture.

14. (original) The method of claim 13, wherein the resistance of the annular region to the pumping current is at least about 50 times greater than the resistance of the central aperture to said pumping current.

15. (original) The method of claim 13, wherein at least 70% of the pumping current flows through the central aperture instead of through the annular region of enhanced resistivity.

16. (original) The method of claim 8, wherein said platform layer consists of InP.

17. (original) The method of claim 16, wherein said substrate consists of InP and said current-confinement layer consists of one of InAlAs and InGaAlAs.

18. (original) The method of claim 16, wherein:

the active region has InGaAsP/InP layers and comprises a plurality of quantum wells which provide a gain spectrum suitable for emitting radiation at a desired

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wavelength; and
the bottom mirror is a semiconductor DBR having a plurality of semiconductor mirror pairs.

19. (original) The method of claim 8, wherein:
electrically-conducting semiconductor layers below the active region are doped to have a first conductivity type and electrically-conducting semiconductor layers above the active region are doped to have a second conductivity type opposite to the first conductivity type;
the first laser portion further comprises a bottom spacer layer disposed between the active region and the bottom mirror; the bottom mirror is a bottom semiconductor DBR;
the electrically-conducting semiconductor layers below the active region comprise the substrate, the bottom DBR, and the bottom spacer; and
the electrically-conducting semiconductor layers above the active region comprise the first top spacer layer, and the top current-spreading layer.
20. (original) The method of claim 1, wherein said platform layer consists of InP.
21. (original) The method of claim 1, wherein the first laser portion further comprises a bottom spacer layer disposed between the active region and the bottom mirror.
22. (original) The method of claim 1, wherein said ion implantation of step (c) employs an implant energy of less than 400keV.
23. (original) The method of claim 22, wherein said ion implantation of step (c) employs oxygen as an implanted species.
24. (original) The method of claim 23, wherein the resistance of the annular region to current conducted through said top current-spreading layer and said platform layer is sufficiently greater than the resistance of the central aperture to said current so that at least about 70% of said current flows through the central aperture .
25. (original) The method of claim 1, wherein the resistance of the annular region to current conducted through said top current-spreading layer and said platform layer is sufficiently greater than the resistance of the central aperture to said current so that at least about 70% of said current flows through the central aperture.
26. (original) The method of claim 1, wherein the combined thickness of the platform and current-confinement layers is less than one-third of the thickness of the buffer layer.
27. (original) The method of claim 1, wherein:
the substrate and layers disposed thereon are part of a wafer sample;
said step (d) further comprising the step of thermally annealing the sample, after said ion implantation, to correct damage to the crystal lattice of the platform layer caused by said ion implantation.

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28. (original) The method of claim 1, wherein:
the substrate and layers disposed thereon are part of a wafer sample; and
the epitaxial regrowth of step (d) is performed with heat sufficient to thermally anneal the sample to correct damage to the crystal lattice of the platform layer caused by said ion implantation.
29. (original) The method of claim 1, wherein the central aperture has either a substantially elliptical or rectangular cross-section to cause polarization of the output of the laser.
30. (original) The method of claim 1, wherein the substrate and layers disposed thereon are part of a wafer sample, further wherein said forming of the current-confinement structure results in native oxide being formed on a top surface of the platform layer, the method further comprising the step of:
(e) after said forming of the current-confinement structure but before performing the epitaxial regrowth of step (d), removing the native oxide from the surface of the platform layer by at least one of desorption, in-situ etching, and in-situ cleaning.
31. (original) The method of claim 1, wherein the first top spacer layer and a second top spacer layer comprising the top current-spreading layer form a composite top spacer layer, the composite top spacer layer having a thickness selected so as to optimally position the active region axially within the laser structure.
32. (original) A method for fabricating a surface-emitting laser having current confinement, the method comprising the steps of:
(a) providing a first laser portion having a substrate, a semiconductor active region, and a bottom mirror disposed between the substrate and the active region;
(b) epitaxially growing a first top spacer layer on the active region, the first top spacer layer comprising a current-spreading buffer layer disposed on the active region and a current-confinement layer disposed on the buffer layer;
(c) forming, in the current-confinement layer, a current-confinement structure having an annular region of enhanced resistivity and a central aperture of comparatively lower resistivity, said step of forming comprising the step of performing ion implantation; and
(d) after said forming of the current-confinement structure, performing epitaxial regrowth to form a top current-spreading layer on the current-confinement layer.
33. (original) The method of claim 32, wherein the thickness of the current-confinement layer is less than one-third of the thickness of the buffer layer.
34. (original) The method of claim 32, further comprising the steps of:
(e) performing epitaxial regrowth to form a contact-facilitating layer on the top current-spreading layer;
(f) forming a metal contact on the contact-facilitating layer, wherein:
the contact-facilitating layer is for electrically coupling the metal contact to the top current-spreading layer;

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the top current-spreading layer is sufficiently conductive to conduct pumping current from the metal contact laterally through the top current-spreading layer to the central aperture of the current-confinement structure; and the annular region has an enhanced resistivity sufficiently greater than that of the central aperture so that most of the pumping current from the metal contact conducted through the top current-spreading layer is blocked by the annular region and forced through the central aperture; and

(g) forming a top mirror disposed axially above the first top spacer layer to complete a resonant cavity surrounding the active region.

35. (original) The method of claim 34, wherein the resistance of the annular region to the pumping current conducted through said top current-spreading layer from the metal contact is sufficiently greater than the resistance of the central aperture to said pumping current so that at least about 70% of said pumping current flows through the central aperture instead of through the annular region of enhanced resistivity.

36. (original) The method of claim 32, wherein said platform layer and said substrate consist of InP and said current-confinement layer consists of one of InAlAs and InGaAlAs.

37. (original) The method of claim 32, wherein said ion implantation of step (c) employs an implant energy of less than 400keV and employs oxygen as an implanted species.

38. (original) The method of claim 32, wherein:
the substrate and layers disposed thereon are part of a wafer sample;
said step (d) further comprising the step of thermally annealing the sample, after said ion implantation, to correct damage to the crystal lattice of the platform layer caused by said ion implantation.

39. (original) A method for fabricating a surface-emitting laser having current confinement, the method comprising the steps of:

- (a) providing a first laser portion having a substrate, a semiconductor active region, and a bottom mirror disposed between the substrate and the active region;
- (b) epitaxially growing a top spacer layer on the active region, the top spacer layer comprising a current-spreading buffer layer disposed on the active region, a current-confinement layer disposed on the buffer layer, and a current-spreading platform layer disposed on the current-confinement layer, wherein the combined thickness of the platform and current-confinement layers is less than the thickness of the buffer layer;
- (c) forming, in the current-confinement layer, a current-confinement structure having an annular region of enhanced resistivity and a central aperture of comparatively lower resistivity, said step of forming comprising the step of performing ion implantation; and
- (d) forming a contact-facilitating layer on the platform layer.

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40. (original) The method of claim 39, wherein the step (d) comprises the step of epitaxially regrowing the contact-facilitating layer on the platform layer after step (c), the method further comprising the steps of:

- (e) forming a metal contact on the contact-facilitating layer, wherein:
 - the contact-facilitating layer is for electrically coupling the metal contact to the platform layer;
 - the platform layer is sufficiently conductive to conduct pumping current from the metal contact laterally through the platform layer to the central aperture of the current-confinement structure; and
 - the annular region has an enhanced resistivity sufficiently greater than that of the central aperture so that most of the pumping current from the metal contact conducted through the platform layer is blocked by the annular region and forced through the central aperture; and
- (g) forming a top mirror disposed axially above the first top spacer layer to complete a resonant cavity surrounding the active region.

41. (original) The method of claim 40, wherein the resistance of the annular region to the pumping current conducted through said platform layer from the metal contact is sufficiently greater than the resistance of the central aperture to said pumping current so that at least about 70% of said pumping current flows through the central aperture instead of through the annular region of enhanced resistivity.

42. (original) The method of claim 40, wherein said platform layer and said substrate consist of InP and said current-confinement layer consists of one of InAlAs and InGaAlAs.

43. (original) The method of claim 39, wherein said ion implantation of step (c) employs an implant energy of less than 400keV and employs oxygen as an implanted species.

44. (original) The method of claim 39, wherein the step (d) comprises the step of epitaxially regrowing the contact-facilitating layer on the platform layer after step (c).

45. (original) The method of claim 44, wherein the combined thickness of the platform and current-confinement layers is less than one-third of the thickness of the buffer layer.

46. (original) The method of claim 39, wherein:

- step (d) comprises the step of epitaxially growing the contact-facilitating layer on the platform layer after step (b) and before step (c); and
- the combined thickness of the platform, current-confinement, and contact-facilitating layers is less than the thickness of the buffer layer.

47. (original) The method of claim 39, wherein:

- step (d) comprises the step of epitaxially growing the contact-facilitating layer on the platform layer after step (b) and before step (c); and
- the combined thickness of the platform, current-confinement, and contact-facilitating layers is less than one-third of the thickness of the buffer layer.

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